09/864682

PTO/SB/17 (12-04v2) Approved for use through 7/31/2006. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE viction Act of 1995, no person are required to respond to a collection of information unless it displays a valid OMB control number. Complete if Known Effective on 12/08/2004. Patent#: 6,918,072 Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818). Application Number Issued: July 12, 2005 FEE TRANSMITTAL Filing Date First Named Inventor Timothy B. Cowles For FY 2005 **Examiner Name** D. Ton Applicant claims small entity status. See 37 CFR 1.27 2133 Art Unit M4065.0559/P559-A **TOTAL AMOUNT OF PAYMENT** 100.00 Attorney Docket No. METHOD OF PAYMENT (check all that apply) None Check x Credit Card Money Order Other (please identify): Dickstein Shapiro Morin & Oshinsky LLP Deposit Account Number: 04-1073 Deposit Account Name: For the above-identified deposit account, the Director is hereby authorized to: (check all that apply) Charge fee(s) indicated below Charge fee(s) indicated below, except for the filing fee Charge any additional fee(s) or underpayment of Credit any overpayments fee(s) under 37 CFR 1.16 and 1.17 **FEE CALCULATION** 1. BASIC FILING, SEARCH, AND EXAMINATION FEES **SEARCH FEES FILING FEES EXAMINATION FEES Small Entity Small Entity Small Entity Application Type** Fee (\$) Fee (\$) Fee (\$) Fee (\$) Fees Paid (\$) Fee (\$) Fee (\$) Utility 300 150 500 250 200 100 Design 200 100 100 50 130 65 Plant 200 100 300 150 160 80 Reissue 300 500 600 150 250 300 200 Provisional 100 0 0 0 0 2. EXCESS CLAIM FEES Small Entity Fee (\$) Fee (\$) Fee Description Each claim over 20 (including Reissues) 50 25 Each independent claim over 3 (including Reissues) 200 100 Multiple dependent claims 180 360 **Total Claims** Extra Claims Fee Paid (\$) **Multiple Dependent Claims** Fee (\$) Fee Paid (\$) Fee (\$) Fee Paid (\$) Indep. Claims **Extra Claims** Fee (\$) 3. APPLICATION SIZE FEE If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s). **Extra Sheets** Number of each additional 50 or fraction thereof Fee Paid (\$) **Total Sheets** Fee (\$) - 100 = /50 (round up to a whole number) x 4. OTHER FEE(S) Fees Paid (\$) Non-English Specification, \$130 fee (no small entity discount) Other (e.g., late filing surcharge): 1811 Certificate of correction 100.00 SUBMITTED BY Registration No. Signature 28,371 Telephone (202) 828-2232 (Attorney/Agent)

Name (Print/Type)

Thomas J. D'Amico



December 1, 2005

Date



Docket No.: M4065.0559/P559-A

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:

Timothy B. Cowles, et al.

Patent No.: 6,918,072

Issued: July 12, 2005

For: CIRCUIT AND METHOD FOR TIME-

EFFICIENT MEMORY REPAIR

## REQUEST FOR CERTIFICATE OF CORRECTION PURSUANT TO 37 CFR 1.322 & 1.323

Attention: Certificate of Correction Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentees noted typographical errors which should be corrected.

In the Specification, Applicants made the following errors that should be corrected:

Column 1, line 55, "256 chip" should read --256 chips--;

Column 13, line 2, "address" should read --addresses--; and

Column 14, line 39, "illustrates" should read --illustrate--.

12/02/2005 HDEMESS1 00000023 6918072

01 FC:1811

100.00 DP

<sub>6</sub> 2005

Patent No.: 6,918,072 Docket No.: M4065.0559/P559-A

In the Claims, the PTO made the following errors that should be corrected:

Claim 55, column 21, line 18, "redundant, planes" should read --redundant planes--; and

Claim 68, column 22, line 31, "scoring" should read --storing--.

In the Other Publications portion of the References Cited section the PTO made the following errors that should be corrected:

"Micron Technology, Inc.; "Synchronous DRAM" Data Sheets; 128Mb: x32 SDRAM: x32 DRAM, 128MbSDRAMx32, p65—Rev. Sep. 2000, pp. 1-52."

## Should Read

--Micron Technology, Inc.; "Synchronous DRAM" Data Sheets; 128Mb: x32 SDRAM, 128Mb: x32 DRAM, 128MbSDRAMx32, p65 - Rev. Sep. 2000, pp. 1-52.--.

The errors were both found in the application as filed by Applicants and made by the PTO. Please charge our Credit Card in the amount of \$100.00 covering the fee set forth in 37 CFR 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

The errors now sought to be corrected are inadvertent typographical errors the correction of which does not involve new matter or require reexamination.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Patent No.: 6,918,072 Docket No.: M4065.0559/P559-A

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0677/P677-A.

Dated: December 1, 2005

Respectfully submitted,

Thomas J. D'Amico

Registration No.: 28,371

Rachael Lea Leventhal

Registration No.: 54,266

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicants

DEC

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. (Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page \_1\_ of \_1

PATENT NO.

6,918,072

APPLICATION NO.

09/864,682

ISSUE DATE

July 12, 2005

INVENTOR(S)

Timothy B. Cowles, et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification, the following errors are corrected:

Column 1, line 55, "256 chip" should read --256 chips--;

Column 13, line 2, "address" should read --addresses--; and

Column 14, line 39, "illustrates" should read --illustrate--.

In the Claims the following errors are corrected:

Claim 55, column 21, line 18, "redundant, planes" should read --redundant planes--; and

Claim 68, column 22, line 31, "scoring" should read --storing--.

In the Other Publications portion of the References Cited section the following errors are corrected:

"Micron Technology, Inc.; "Synchronous DRAM" Data Sheets; 128Mb: x32 SDRAM: x32 DRAM, 128MbSDRAMx32, p65—Rev. Sep. 2000, pp. 1-52."

Should Read

--Micron Technology, Inc.; "Synchronous DRAM" Data Sheets; 128Mb: x32 SDRAM, 128Mb: x32 DRAM, 128MbSDRAMx32, p65 - Rev. Sep. 2000, pp. 1-52.--.